UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,082,302 B1

APPLICATION NO. : 10/728875

: July 25, 2006 DATED INVENTOR(S) : Roger Maitland et al.

> It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, line 48, "...TBs may be stored..." should read --...TBs may be stored...-;

Column 10, line 64, "...RAMs is fficient." should read --...R.AMs is efficient.--;

Column 11, line 61, "...NDCH,..." should read --...N_{DCH},...-;
Column 11, line 64, "...NDCH,..." should read --...N_{DCH},...-;
Column 12, line 1, "...NDCH, can be etermined..." should read --...N_{DCH}, can be determined ... --:

Column 12, line 5, "...NDCH,..." should read --...NDCH,...-;

Column 17, line 48, "...FIG. 1C,..." should read --...FIG. 11C,...-; Column 18, line 12, "...FIG. 1C,..." should read --...FIG. 11C,...-;

Column 23, line 50, "...t' start = t_{center} -S_{win}/2 + t_{offset}..." should read --...t' start = t'_{center} -S_{win}/2

+ t_{offset}...--;

Column 24, claim 1, line 60, "...with each group of corresponding payload sequences..." should be --...from each at least one payload sequence, each...-; and Column 26, claim 8, line 21, "...group or corresponding..." should read --...group of corresponding...-.

Signed and Sealed this

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Twenty-seventh Day of February, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office